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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Youfeng Wu	
	Filing Date: Herewith	Group: Unknown

U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
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FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

2945	Calder, B., et al., "Value Profiling", IEEE, Proceedings of Micro-30, 11 pgs., (Dec. 1-3, 1997)
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2945	Gallagher, D.M., et al., "Dynamic Memory Disambiguation Using the Memory Conflict Buffer", ASPOLS-VI Proceedings, Vol. 29, 183-193, (Nov. 1994)
2945	Steffan, J.G., et al., "Architectural Support for Thread-Level Data Speculation", Computer Science Technical Report, Computer Science Department School of Computer Science, Carnegie Mellon University, CMU-CS-97-188, 1-41, (Nov. 1997)
2945	Steffan, J.G., et al., "The Potential for Using Thread-Level Data Speculation to Facilitate Automatic Parallelization", HPCA-4, 1-12, (Feb. 1-4, 1998)
2945	Tsia, J., et al., "The Superthreaded Processor Architecture", 1-40.
2945	Vijaykumar, T.N., et al., "Task Selection for a Multiscalar Processor", 31st International Symposium on Microarchitecture, 12 pgs., (Dec. 1998)

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**INFORMATION DISCLOSURE
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(Use as many sheets as necessary)

Complete if Known

Application Number 09/522510

Filing Date March 10, 2000

First Named Inventor Wu, Youfeng

Group Art Unit 2782

Examiner Name Unknown

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Sheet 1 of 1

Attorney Docket No: 00884.258US1

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Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
NS	US-5,845,101	12/01/1998	Johnson, William , et al	395	383	05/13/1997
NS	US-5,845,103	12/01/1998	Sodani, Avinash , et al	395	392	06/13/1997

FOREIGN PATENT DOCUMENTS

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William A. Wood

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